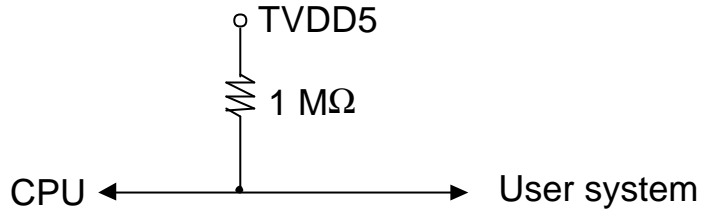


APPENDIX C. RTE-V850E/GP1-IE USER INTERFACE CIRCUITS

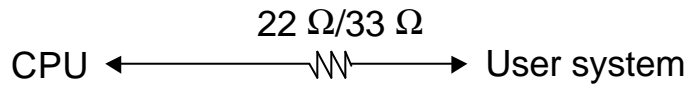
P00 to P07, P10 to P17, P20 to P24, P30 to P37, P40 to P47, P50 to P57, P60 to P64, P90 to P93, P100 to P107, P110 to P117, P120 to P126, P130 to P137, P140 to P147, P150 to P157, P160 to P167, P170 to P177, P180 to P187



CLK_DBG, SYNC, AD0_DBG, AD1_DBG, AD2_DBG, AD3_DBG, MODE_DBG



TRIG_DBG, RESET0, IOCLK (33 Ω only for IOCLK)



ANI00 to ANI15

