RTE-V830-PC SUPPLEMENTARY MANUAL NO.2 (REV.1.0)

1. WHEN THE TIMER INTERRUPT ON THE BOARD IS USED BY APPLICATIONS

This manual describes a method of developing an application using the timer interrupt of the SCC2691 on the board of the RTE-V830-PC as the maskable interrupt, and its restrictions.

1.1. PREPARATION

In order to use the timer interrupt of the SCC2691 as the maskable interrupt, the following devices must be installed on the RTE-V830-PC:

1) M493PLD04AZ or PLD04AZ (PLD number at U17 on the board)
2) Multi ROM monitor ROM Version 1.xx (Version 1.13 or higher)

1.2. RESTRICTIONS AND NOTES

In order to use a timer interrupt, the following restrictions and notes are imposed:

1) Turn on 3 and 4 of SW1 on the board.   By this operation, the Multi monitor ROM does not initialize the timer.   Therefore, functions of Multi related to measuring the time, including a profile function, cannot be used.

2) Any connection to the Multi cannot be made in RS-232C.   Use the ISA bus to connect to the Multi.

3) When some action is taken for the RTE-V830-PC from the Multi debugger, an NMI interrupt for communication occurs on the RTE-V830-PC.   If this interrupt conflicts with an interrupt from SCC2691 (timer or serial interrupt), the interrupt from SCC2691 may be discarded.

4) When developing an application using a maskable interrupt, by using the Multi, keep in mind the several precautions.   For details, see the RTE-V830-PC Supplementary Manual No.1.